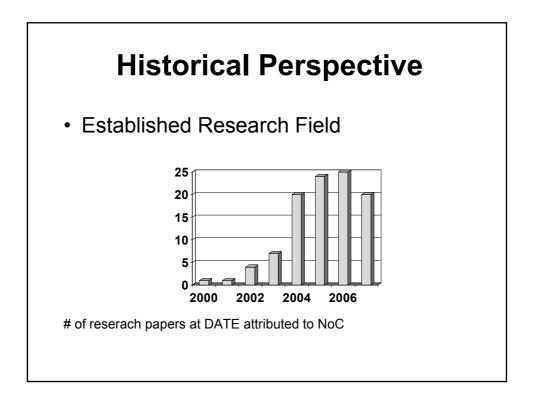
Overview	
9:30 State of the art, 10:00 NoCs in the context of application organization, 11:00-11:30 Break	A. Jantsch ns and memory P. van der Wolf
11:30 NoC Middleware, 13:00 – 14:30 Lunch	L. Benini
14:30 Throughput-driven NoC design, 15:30 Quality of service in NoCs, 16:00 – 16:30 Break	T.M. Pinkston K. Goosens
16:30 Quality of service in NoCs cont'd 17:00 ARTERIS NoC power management, C. Janac 17:30 The Spidergon NoC, M. Coppola	



Topic of Research

- Topology
- Routing, switching, flow control
- QoS
- Clocking
- Design tools, mapping
- Programming model
- Platform services
- Traffic generation
- Debug, diagnosis, testing

Topology

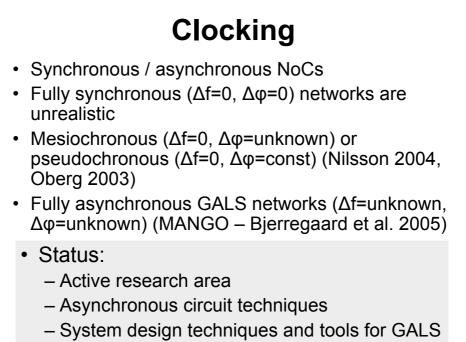
- Fat tree (SPIN)
- Torus (Dally&Towles)
- Ring (Proteo, Octagon)
- Mesh
- Irregular and application specific
- Status:
 - Mesh for a general platform
 - Irregular for application specific architectures

Routing, Switching, Flow control

- · Deterministic / adaptive routing
- Dimension order / source based addressing
- Wormhole switching
- Credit based flow control
- Status:
 - Deterministic routing
 - Wormhole switching

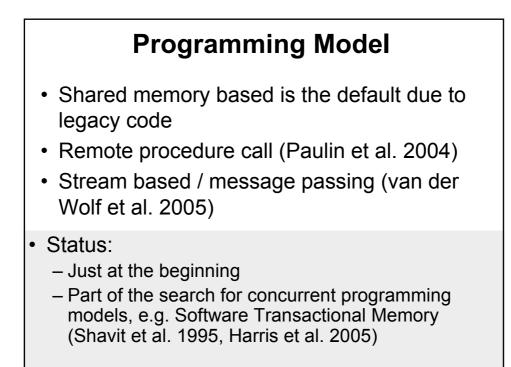
Quality of Service

- Combining Best Effort (BE) and Guaranteed Bandwidth (GB) traffic
- Time Division Multiplexing (TDM) based (Aetherial, Nostrum)
- Priority based QoS (Bolotin 2004)
- QoS on Asynchronous networks (Bjerregaard et al. 2005)
- Mapping with QoS guarantees (Murali et al. 2005)
- Performance analysis for predictable systems (Huang et al. 2007)
- Abstract performance models of NoC platforms (Jantsch 2006)
 - Status:
 - Very active research area
 - Aiming at architecture+mapping tools+system methodology for predictable, efficient systems



NoCs

Design Tools Router and NI configuration, resource allocation, mapping (Murali et al. 2004, 2005, 2006, Hansson et al. 2005, Hu et al. 2003) Communication synthesis (Lu et al. 2006) Functional verification (Schmaltz et al. 2004) Performance analysis (Huang et al. 2007) Status: Very active research area and by far not completed Application domain specific tools More system level tools connection to application and programming models Many specialized point tools missing



Platform Services

- Resource management (Nollet et al. 2005)
- Monitoring and diagnostics (Ciordas et al. 2006)

Status:

- Just at the beginning
- What shall be part of the NoC platform?

Traffic Generation

- Real application traffic: None
- Stochastic traffic (Varatkar et al 2004, Soteriou et al. 2006)
- Status:
 - No good real applications
 - Sophisticated stochastic traffic models
 - Benchmark initiative started

Testing, Diagnosis, Debug

- Network centric degug (Goossens et al. 2007)
- NoC based testing methodology (Petersen et al 2007)

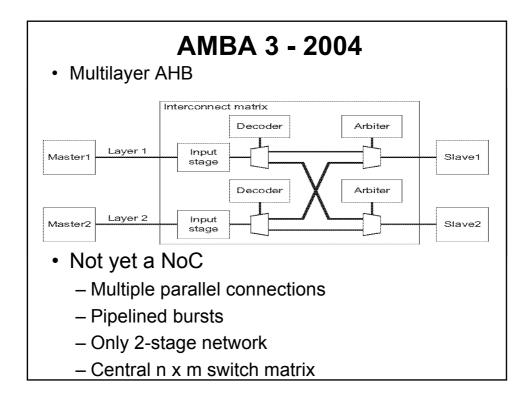
• Status:

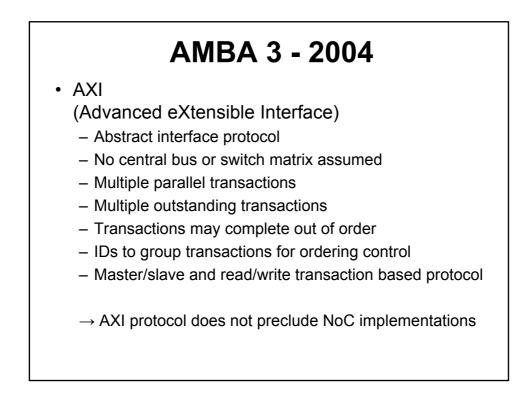
- Off and on-line testing is being address
- Debugging aids are integrated into the network
- On-line monitoring and diagnosis is emerging

Development in Industry

- Full fledged NoC approach:
 - Arteris: start-up focusing on NoC
 - NXP's Aetherial
 - ST's Spidergon
- Evolutionary development from bus to network:
 - AMBA protocol family
 - Sonics
 - OCP
 - STBus
 - CoreConnect
 - IBM's Cell interconnect
 - Xilinx
 - etc.

AMBA ARM's Advanced Microcontroller Bus Interface • AMBA 1995: - APB (Advanced Peripheral Bus) - ASB (Advanced System Bus) · Multiple masters · Pipelined operations AMBA 2 1999: ٠ - AHB (Advanced High Performance Bus) · Multiple masters · Pipelined operations · Burst transactions · Split transactions, multiple outstanding transactions · Single cycle master hand-over · Exclusive bus control · Single- centralized decoder for slave select





References -1-

[Alho and Nurmi 2003] M. Alho and J. Nurmi, "Implementation of Interface Router IP for Proteo Network-on-Chip", Proc. The 6th IEEE International Workshop on Design and Diagnostics of Electronics Circuits and Systems (DDECS'03), Poznan, Poland, 14-16 April 2003.

[Bjerregaard 2005] Tobias Bjerregaard and Jens Sparso, "A Router Architecture for Connection-Oriented Service Guarantees in the MANGO Clockless Network-on-Chip, booktitle = Proceedings of the conference on Design, Automation and Test in Europe - Volume 2", pp. 1226 - 1231, March 2005.

[Bjerregaard 2006] Tobias Bjerregaard and Shankar Mahadevan, "A Survey of Research and Practice of Network-on-Chip", ACM Computing Surveys, 2006.
 [Bolotin 2004] E. Bolotin, I. Cidon, R. Ginosar, and A. Kolodny, "QNoC: QoS architecture and design process for network on chip", Journal of Systems Architecture, vol. 50, no. 2-3, pp. 105-128, February 2004.

[Ciordas et al. 2006] Cualin Ciordas, Kees Goossens, Twan Basten, Andrei Radulescu, and Andre Boon, "Transaction Monitoring in Networks on Chip: The On-Chip Run-Time Perspective", Proc. Symposium on Industrial Embedded Systems (IES), Antibes, France, IEEE, Oct 2006.

[Dally&Towles 2001] William J. Dally and Brian Towles, "Route Packets, Not Wires: On-Chip Interconnection Networks", Proceedings of the 38th Design Automation Conference, June 2001

[Gooosens 2002] K. Goossens, J. van Meerbergen, A. Peeters, and P. Wielage, "Networks on silicon: Combining best-effort and guaranteed services", Proceedings of the Design Automation and Test Conference, March 2002.

[Goossens et al. 2007] Kees Goossens, Bart Vermeulen, Remco van Steeden and Martijn Bennebroek, "Transaction-Based Communication-Centric Debug", Proccedings opf the First International Symposium on Networks on Chip, May 2007.

[Petersen et al. 2007] Kim Petersen and Johnny Oberg, "Toward a scalable test methodology for 2D-mesh network-on-chips", Proceedings of the Conferences on Design Automation and Test in Europe, April 2007.

